

**AMENDMENTS TO THE CLAIMS:**

Please cancel claims 1 – 5, without prejudice or disclaimer of their subject matter. This listing of claims will replace all prior versions and listings of claims in the application:

1. – 5. (Canceled)

6. (Original) A method of manufacturing a semiconductor device, comprising the steps of:

forming a soluble thin film which is soluble in a dissolving liquid on a film to be processed which is formed on a semiconductor substrate;

forming a first mask pattern on the soluble thin film;

forming a mask layer on the first mask pattern such that an exposed portion of the soluble thin film is covered with the mask layer;

etching back the mask layer such that an upper face of the first mask pattern is exposed and the portion of the mask layer covering the exposed portion of the soluble thin film remains to form a second mask pattern;

removing the first mask pattern;

etching the soluble thin film and the film to be processed using the second mask pattern as a mask; and

dissolving the etched soluble thin film in the dissolving liquid, thereby lifting off the second mask pattern from the film to be processed.

7. (Original) A method according to claim 6, wherein the soluble thin film contains at least one compound selected from the group consisting of tungsten oxide, aluminum oxide, titanium oxide, and titanium nitride.

8. (Original) A method according to claim 6, wherein the dissolving liquid is either water or alkaline solution.

9. (Original) A method according to claim 6, wherein the step of forming the first mask pattern comprises:

forming a first mask layer;

forming a resist film with a thickness of 0.3  $\mu\text{m}$  or less on the first mask layer;

patterning the resist film by using photo-lithography technique to form a resist pattern;

and

etching the first mask layer using the resist pattern as a mask, thereby forming the first mask pattern.

10. (Original) A method according to claim 7, wherein the step of etching the soluble thin film and the film to be processed comprises forming a contact hole in the film to be processed.

11. (Original) A method of manufacturing a semiconductor device, comprising the steps of:

forming a soluble thin film which is soluble in a dissolving liquid on a first insulating film which is formed on a semiconductor substrate;

forming a resist pattern on the soluble thin film;

etching the soluble thin film using the resist pattern as a mask to form a wiring groove;

removing the resist pattern after the step of forming the wiring groove;

forming a wire in the wiring groove in an embedding manner;

forming a second insulating film on the wiring and the soluble thin film;

forming a window portion in the second insulating film such that the soluble thin film is exposed at a bottom of the window portion; and

dissolving the soluble thin film in the dissolving liquid to remove the soluble thin film.

12. (Original) A method according to claim 11, wherein the soluble thin film contains at least one compound selected from the group consisting of tungsten oxide, aluminum oxide, titanium oxide, and titanium nitride.

13. (Original) A method according to claim 11, wherein the dissolving liquid is either water or alkaline solution.

14. (Original) A method according to claim 11, wherein the step of removing the soluble thin film comprises causing the dissolving liquid to contact with the soluble thin film through the window portion.

15. (Original) A method according to claim 11, further comprising the step of forming a lower wiring in the first insulating film in an embedding manner prior to the step of forming the soluble thin film.

16. (Original) A method according to claim 15, further comprising the step of forming a via hole reaching the lower wiring in a bottom of the wiring groove between the step of forming the wiring groove and the step of forming the wire in the embedding manner, wherein the step of forming the wire in the embedding manner comprises forming a plug electrode in the via hole.

17. (Original) A method of manufacturing a semiconductor device, comprising the steps of:

forming an organosilicon compound film on a semiconductor substrate;  
forming a silicon oxide film on the organosilicon compound film;  
forming a resist pattern on the silicon oxide film;  
etching the organosilicon compound film and the silicon oxide film using the resist pattern as a mask; and

dissolving the etched silicon oxide film in the dissolving liquid, thereby lifting off the resist pattern from the organosilicon compound film.

18. (Original) A method according to claim 17, wherein the silicon oxide film is formed by supplying gas containing activated oxygen on a surface of the organosilicon compound film.

19. (Original) A method according to claim 17, wherein the dissolving liquid is diluted hydrofluoric acid.

20. (Original) A method according to claim 17, wherein the organosilicon compound film is a SOG film.